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2812

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Daniel Xu et al.

Serial No.: 10/633,881

Filed: August 4, 2003

For: Forming Planarized
Semiconductor Structures

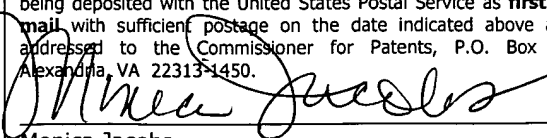
§
§ Art Unit: 2812
§
§ Examiner: Alexander G. Ghyka
§
§ Docket: ITO.0024US
§ P14742
§
§ Assignee: Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 05252005

Sir:

In response to the office action mailed June 6, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: August 4, 2005
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Monica Jacobs